



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) James John Casto et al.

Title: INTEGRATED CIRCUIT PACKAGE INCORPORATING  
PROGRAMABLE ELEMENTS

Application No.: 09/484,311

Filed: January 18, 2000

Examiner: Eugene Lee

Group Art Unit: 2815

Atty. Docket No.: 1001-0087

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PATENT  
#6 Election  
TECHNICAL CENTER 2300  
10/25/01

October 12, 2001

Box Non-Fee Amendment  
COMMISSIONER FOR PATENTS  
Washington, DC 20231


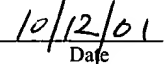
**RESPONSE TO RESTRICTION REQUIREMENT**

This paper is responsive to an Office action dated September 25, 2001, having a shortened statutory period for response set to expire October 25, 2001.

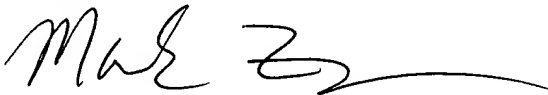
Election of Invention

Applicant appreciates the Examiner's courtesy in the telephone conversation of October 12, 2001, in which the Examiner confirmed that claims 37 and 38 should be in Group I.

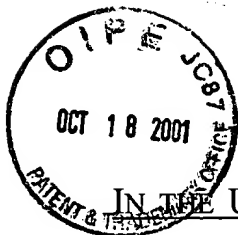
In response to the Examiner's restriction requirement, the undersigned hereby elects Group I, claims 1 thru 27 and 37-38.

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on the date shown below.	
 Mark Zagorin	 10/12/01 Date

Respectfully submitted,



Mark Zagorin, Reg. No. 36,067  
Attorney for Applicant(s)  
(512) 347-9030  
(512) 347-9031 (fax)



PATENT

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**PRELIMINARY AMENDMENT**

Prior to the first action on the merits, please amend the above-identified application as follows:

*In the Specification*

Please delete the paragraph beginning on page 4, line 22, and substitute therefore the following paragraph:

*Qu*  
Fig. 1 shows a prior art approach for providing voltage and frequency settings.

Please delete the paragraph beginning on page 7, line 27, and substitute therefore the following paragraph:

*QJ*  
Each of the fuses 301-304 and 315-318 include a fusible link coupling each end of the fuse formed by the metal trace or other suitable conductor. Referring to Fig. 3B, the programming shown provides a binary setting of 1-0-1-0 as the value of the fuses, where 1 is Vcc and 0 is Vss. That is, fuses 315 and 317 are blown causing vias 310 and 312 to be coupled only to Vcc. Fuses 302 and 304 are also blown causing vias 311 and 313 to be only coupled to Vss.